



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Lim, Kyu-Nam  
Assignee: Samsung Electronics Co., Ltd.  
Title: Input Buffer Circuit  
Application No.: 09/971,991  
Examiner: L. Nguyen  
Docket No.: AB-984-1C US

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Hmuth

COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(C) WITH FEE

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant(s) wish to call the following documents (copies enclosed) to the attention of the Examiner for the above-identified patent application. The documents were cited in the Korean Patent Office for the corresponding Korean Application No. 10-2000-0007522.

A form PTO-1449 listing these documents is enclosed.

The relevance of any non-English language documents cited herein is explained in the accompanying copy of the .

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
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3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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Respectfully submitted,

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